

200G QSFP56 to 2x100G QSFP56 Direct Attach Cable

Product Features

- Fully compliant to the latest SFF8665 QSFP MSA
- EEPROM in cable assembly
- Maximum link length: up to 3m
- Enables 200Gb/s to 2X100Gb/s transmission
- Selectable: 26AWG, 28AWG, 30AWG
- Single +3.3V power supply
- Operation case temperature: 0 to 70°C
- RoHS2 compliant



Applications

- Data Center, High Performance Computing (HPC)
- Router, Server, Storage, Switch

Absolute Maximum Ratings

Parameter	Unit	Min.	Typical	Max.	Notes
Storage Temperature	°C	-45		80	
Operating Relative Humidity	%			85	
Power Supply not Damaged Voltage	V	-0.5		3.6	

Recommended Operating Conditions

Parameter	Unit	Min.	Typical	Max.	Notes
Operating Case Temperature	°C	0		70	
Power Supply Working Voltage	V	3.135	3.3	3.465	
Bit Rate	Gbps		200		

Characteristics

All performance is defined over the Recommended Operating Environment unless otherwise specified.

	Item	Parameter
Physical	Length	0.5 to 3m
	Cable Colour	Black
Electrical	Resistance	2 ohm Max
	Insulation Resistance	10M ohm Min
SI Performance	SDD21	-16.06dB Min. @13.28 GHz
	SDD11/SDD22	-16.5+2*sqrt(f)dB Max @0.05GHz-4.1GHz -10.66+14*log(f/5.5) dB Max@4.1GHz-19GHz
	SCD22	-22+(20/25.78) *f dB Max@0.01GHz~12.89GHz -15+(6/25.78) *f dB Max@12.89GHz~19GHz
	SCC11	-2dB Max
	SCD21-SDD21	-10dB Max @0.01GHz~12.89GHz -27+(29/22) *f dB Max @12.89GHz~15.7GHz -6.3dB Max @15.7GHz~19GHz
	ICN	3 ≤ IL ≤ 7.65: 5 mV Max 7.65 ≤ IL ≤ 26: 12.75 - 0.49 *f mV Max IL is the value @13.28GHz, f is in GHz
	COM	3 dB Min
	NEXT	-40dB Max

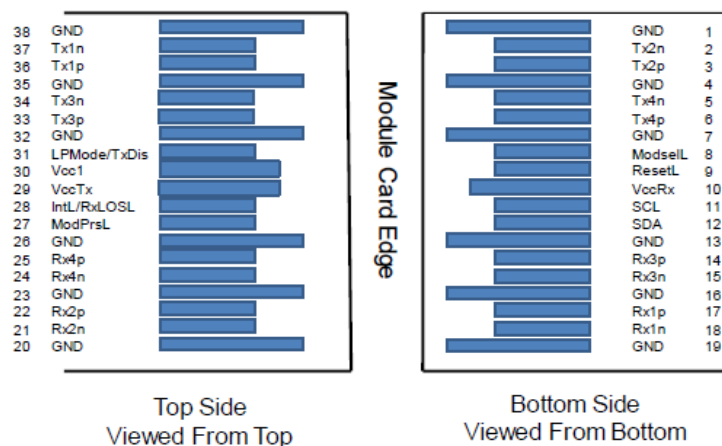
PIN Function Definitions

Pin No.	Symbol	Level / Logic	Description
1	GND		Module Ground
2	Tx2n	CML-I	Transmitter Inverted Data Input
3	Tx2p	CML-I	Transmitter Non-Inverted Data Input
4	GND		Module Ground
5	Tx4n	CML-I	Transmitter Inverted Data Input
6	Tx4p	CML-I	Transmitter Non-Inverted Data Input
7	GND		Module Ground
8	ModSelL	LVTTTL-I	Module Select
9	ResetL	LVTTTL-I	Module Reset
10	VccRx		+3.3V Power Supply for Receiver
11	SCL	LVTTTL-I	2-Wire Serial Interface Clock
12	SDA	LVTTTL-I/O	2-Wire Serial Interface Data Line
13	GND		Module Ground

14	Rx3p	CML-O	Receiver Non-Inverted Data Output
15	Rx3n	CML-O	Receiver Inverted Data Output
16	GND		Module Ground
17	Rx1p	CML-O	Receiver Non-Inverted Data Output
18	Rx1n	CML-O	Receiver Inverted Data Output
19	GND		Module Ground
20	GND		Module Ground
21	Rx2n	CML-O	Receiver Inverted Data Output
22	Rx2p	CML-O	Receiver Non-Inverted Data Output
23	GND		Module Ground
24	Rx4n	CML-O	Receiver Inverted Data Output
25	Rx4p	CML-O	Receiver Non-Inverted Data Output
26	GND		Module Ground
27	ModPrsL	LVTTTL-O	Module Present
28	IntL/RxLOSL	LVTTTL-O	Interrupt. Optionally configurable as RxLOSL via the management interface (SFF-8636).
29	VccTx		+3.3V Power Supply for Transmitter
30	Vcc1		+3.3V Power Supply
31	LPMoDe/TxDiS	LVTTTL-I	Low Power Mode. Optionally configurable as TxDis via the management interface (SFF-8636).
32	GND		Module Ground
33	Tx3p	CML-I	Transmitter Non-Inverted Data Input
34	Tx3n	CML-I	Transmitter Inverted Data Input
35	GND		Module Ground
36	Tx1p	CML-I	Transmitter Non-Inverted Data Input
37	Tx1n	CML-I	Transmitter Inverted Data Input
38	GND		Module Ground

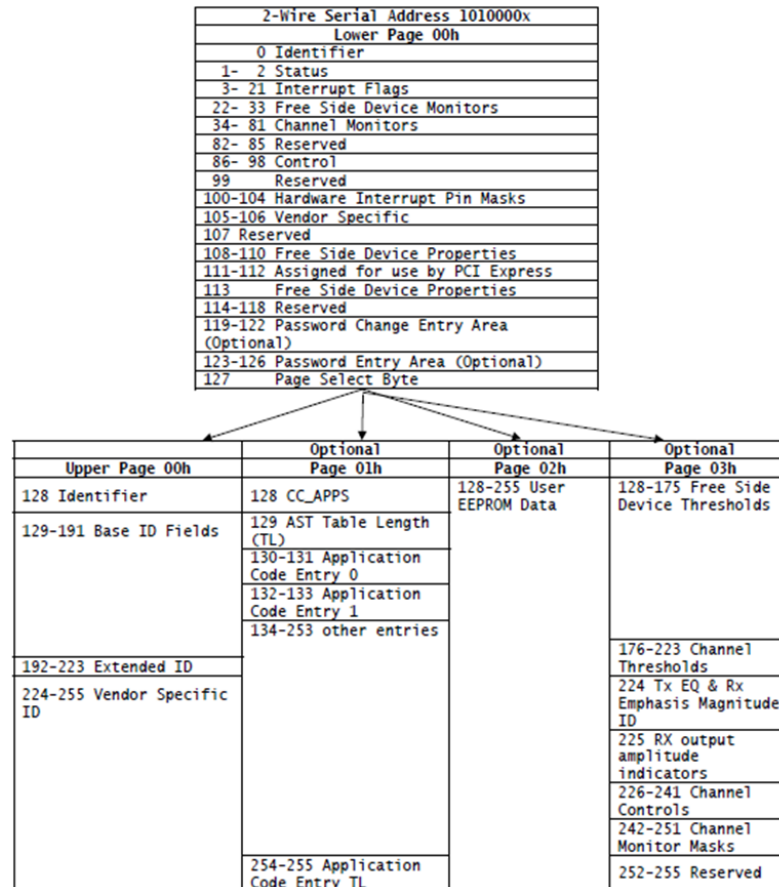
Electrical Pad Layout

For detail mechanical information, please refer to the related document of SFF-8679.



EEPROM Information

The digital diagnostic memory map specific data field define as following. For detail EEPROM information, please refer to the related document of SFF 8636 Rev 2.1.



ESD

This product is specified as ESD threshold 1kV for high speed data pins and 2kV for all other electrical input pins, tested per MIL-STD-883, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module. This product is shipped in ESD protective packaging. It should be removed from the packaging and handled only in an ESD protected environment.

Ordering Information

Ordering P/Ns	Description
DHZZjj-KCCC-xxx	0.5m~3m, 200G QSFP56 break to 2x100G QSFP56 DAC, commercial temperature

XXX	Cable Length
005	005=0.5m
⋮	⋮
020	020=2m
⋮	⋮
030	030=3m

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